Page 190 in the textbook.

Suppose that two processes, P_0 and P_1 access the same global variable `counter` concurrently. P_0 does `counter++` and P_1 does `counter--`

Then, if `counter==5`, after this councurrent execution, its value may be any one of 4,5,6.

The machine instructions for `counter++` and `counter--` are as follows:

```
R1=counter
R1=R1+1
counter=R1

R2=counter
R2=R2-1
counter=R2
```

The above two computations are executed "concurrently," which means that on a single CPU machine they are interleaved in some arbitrary order, but still consistent with the above sequence of executions.

One such interleaving:

P_0 `R1=counter` (R1=5)

P_0 `R1=R1+1` (R1=6)

P_1 `R2=counter` (R2=5)

P_1 `R2=R2-1` (R2=4)

P_0 `counter=R1` (counter=6)

P_1 `counter=R2` (counter=4)